EFS ID:	1064165		
Application Number:	10707205		
Confirmation Number:	1204		
Title of Invention:	SYSTEM AND METHOD TO TEST INTEGRATED CIRCUITS ON WAFER		
First Named Inventor:	Lawrence Pileggi		
Customer Number:	24239		
Filer:	Charles L. Moore/Katie Efland		
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Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Amendment - After Non-Final Rejection	Response_to_Office_Action. pdf	1439615	no	31

Warnings:	
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.